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Semiconductor power module.

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Abstract

In a power module (31) having at least one power device chip (32) and circuits for driving and controlling the power device chip which are incorporated within one and the same package, a heat spreader (39) which is constituted by a material having a heat dissipation property and supports the power device chip on its upper surface is inserted into an opening (41) formed in a control substrate (33) having an upper surface on which patterns for forming the chip driving and controlling circuits and a connection pattern for connection with the power device chip (32) are printed, and the control substrate (33) and the heat spreader (39) inserted into the opening (41) are fixedly supported on a base substrate (34). Thus, the

power device chip (32) and the control substrate (33) can be disposed in plane and reduced in size. 

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(54) **Semiconductor power module**

Leistungshalbleitermodul

Module semi-conducteur de puissance

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- PATENT ABSTRACTS OF JAPAN vol. 017, no. 344 (E-1390) 29 June 1993 & JP-A-05 047 968 (FUJITSU LTD) 26 February 1993

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Description

[0001] The present invention generally relates to power modules and particularly relates to a power module in which at least one power device chip and circuits for driving and controlling the power device chip are incorporated within one and the same package.

[0002] Being small in size and excellent in workability, such a power module in which at least one power device chip, for example, a power transistor chip, an MOS device chip, a diode chip, a thyristor chip, or the like, and circuits for driving and controlling the power device chip are incorporated within one and the same package has been used widely as an inverter power source circuit for composing a three-phase alternating current wave, or a power source circuit for controlling peripheral equipments.

[0003] Since the power device chip generates heat, therefore, there is a case where the heat causes the circuit elements of the built-in circuits for driving and controlling the power device chip to make erroneous operation or to get in failure. In order to protect the circuits for driving and controlling the power device chip from the heat of the power device chip, therefore, the circuits for driving and controlling the power device chip must be separated at a certain extent from the power device chip, and there is a limit in miniaturization of the power module.

[0004] There has been therefore proposed a power module having a two-layer inside structure to reduce the area occupied by the power module, as shown in Figs. 4 and 5. That is, a power module 11 has a two-layer structure of a base substrate 13 and a control substrate 14. The base substrate 13 is constituted by an aluminum substrate or a DBC (direct bonding copper) substrate for supporting power device chips, for example, power transistor chips 12. On the other hand, the control substrate 14 is disposed at a distance above the base substrate 13 and constituted by a printed circuit substrate having printed patterns of circuits for driving and controlling the power transistor chips. An output terminal 15 of the power module is connected to the upper surface of the base substrate 13, and patterns are provided on the base substrate 13 for connection with input/output terminals of the power device chips 12 and the control circuit substrate 14. The control circuit substrate 14 is supported, at a distance above the base substrate 13, by connection terminals 16 which act also as support legs. The input/output terminals of the control circuit substrate 14 are connected to the base substrate 13 through the connection terminals 16. Circuit elements 17 of the circuits for driving and controlling the power transistor chips 12 are mounted on the control circuit substrate 14, and a control terminal 18 is also connected to the control circuit substrate 14. The base substrate 13 and the control circuit substrate 14 are covered with a case cover 19 of synthetic resin. The base substrate 13 is exposed at its lower surface. Alternatively, a heat

sink plate may be attached onto the lower surface of the base substrate 13 if necessary.

[0005] Each transistor chip 12 employs a bare chip in which a base electrode 20 and an emitter electrode 21 are formed on its upper surface, while its lower surface is formed as a collector terminal 22 as a whole, as shown in Fig. 5. The lower surface, that is, the collector terminal 22, of this transistor chip 12 is fixed, through solder 24, onto the upper surface of a heat spreader 23 of metal such as copper having a high heat conductivity, and the lower surface of the heat spreader 23 is fixed onto the base substrate 13 through solder 25. The base and emitter electrodes 20 and 21 of the transistor chip 12 are connected to predetermined patterns 26 and 27 on the upper surface of the base substrate 13 by the wire bonding with aluminum wire 28. The collector terminal 22 is connected to a predetermined pattern (not shown) on the base substrate 13 through the solder 24 and wire bonding (not shown). The transistor chip 12 fixed on the base substrate 13 is coated with a sealing agent 29 of synthetic resin, as shown in Fig. 4.

[0006] Having such a two-layer structure, the power module can be reduced in its area, but it increases its height. Further, there is a defect that it is necessary to provide means for supporting the control circuit substrate 14 above the base substrate 13 and means for electrically connecting the base and control circuit substrates to each other. The structure is therefore made complicated, and the number of assembling steps increases. Further, the base substrate 13 is constituted by an aluminum substrate or a DBC substrate from the point of view of requiring high heat dissipation and insulation, resulting in increased material cost.

[0007] In PATENT ABSTRACTS OF JAPAN, Volume 16, No. 344 (E-1239) 24/07/92; and JP-A-4105350, and PATENT ABSTRACTS OF JAPAN, Volume 16, No. 360 (E-1243) 04/08/92; and JP-A-4112560, there are described chip-mounting arrangements wherein an opening is provided in the form of either an angular hole or a through hole within which a heat spreader, upon which a power device chip is supported, can be inserted, so that the spreader and the device chip are fixed in the same plane on a base substrate.

[0008] In PATENT ABSTRACTS OF JAPAN, Volume 15, No. 241 (E1080) 21/06/91; and JP-A-3078255, an arrangement is described wherein a metal plate provides a base for a semi-conductor chip.

[0009] In Microelectronics Packaging Handbook, R. Tummala et al., Van Nostrand Reinhold, New York, US, 1988; pp. 476-490; there is described a process for fabricating a multilayer ceramic substrate.

[0010] In EP-A-0 468 475, there is disclosed a DBC plate for fixing thereon a semi-conductor pellet.

[0011] It would be desirable to provide a power module which is simplified in the structure of connection between a base substrate supporting power device chips and a control substrate having printed patterns of circuits for driving and controlling the power device chips.

[0012] It would further be desirable to provide a power module in which power device chips and circuits for driving and controlling the power device chips are arranged adjacently to each other substantially on one and the same plane.

[0013] According to the present invention, there is provided a power module as defined in Claim 1.

[0014] With this structure, the power device chip and the control substrate are supported substantially in one and the same plane so that it is possible to reduce the height to half in comparison with a conventional two-layer system. Further, since the heat spreader is fixed on the base substrate, the heat of the power device chip on the heat spreader is transmitted through the heat spreader to the base substrate so as to be dissipated from the base substrate, so that there is no fear that the heat is given to the circuit elements on the control substrate. It is therefore possible to dispose the circuit elements on the control substrate close to the power device chip to thereby make it possible to reduce the area of the control substrate, so that the power module can be reduced in size on a large scale in comparison with conventional ones.

[0015] Other features and advantages of the present invention will be apparent from the following description of preferred embodiments.

Fig. 1 is a vertical sectional view illustrating a main portion of a first arrangement of a power module, but which does not constitute an embodiment of the present invention;

Fig. 2 is an enlarged vertical sectional view illustrating a power device chip portion in the arrangement of Fig. 1, but viewed in a different direction;

Fig. 3 is an enlarged vertical sectional view illustrating a power device chip portion in an embodiment of the power module according to the present invention;

Fig. 4 is a vertical sectional view illustrating a main portion of an example of the conventional power module; and

Fig. 5 is an enlarged vertical sectional view illustrating a power device chip portion in the example of Fig. 4.

[0016] Referring to the drawings, a preferred embodiment of the present invention will be described under. In the Fig. 1 arrangement and the embodiment, the constituent elements the same as or equivalent to each other are referenced correspondingly, and the description thereof is not repeated.

[0017] First, referring to Figs. 1 and 2 an arrangement will be described, which does not constitute an embodiment of the present invention. A power module 31 is supported by a base substrate 34 and a case cover 35 provided so as to cover the upper surface of the base substrate 34 with a predetermined space therebetween. The base substrate 34 supports thereon a sheet of con-

trol substrate 33 which is a printed circuit substrate on which patterns to be connected to the terminals of power device chips 32 and patterns of circuits for driving and controlling the chips are printed. Circuit elements 36 of the driving and controlling circuits are mounted on the control substrate 33 in the same manner as in the above-mentioned conventional example, and control and output terminals 37 and 38 of the power module 31 are connected to the control substrate 33. The base substrate 34 is constituted by a material such as an aluminum plate or the like having a property of good heat dissipation. A heat sink plate may be attached onto the back surface of the base substrate 34 in order to make the effect of heat dissipation higher.

[0018] As shown more in detail in Fig. 2, an opening 41 for inserting therein a heat spreader 39 for supporting each power device chip 32 on its upper surface is formed in the control substrate 33. The heat spreader 39 is made of material, such as aluminum nitride, alumina, or the like, which is superior in heat dissipation and insulation, and the lower surface of the heat spreader 39 is attached to the base substrate 34 through a bonding agent 42. The heat spreader 39 is provided at its upper surface with a metal portion 43 treated with DBC. This metal portion 43 is disposed so that its lower, copper portion is made to contact with the heat spreader 39 and its upper, nickel-plated portion is fixed to a collector terminal 44 of the power device chip 32 through solder 45. Each power device chip 32 has a base electrode 46 and an emitter electrode (not-shown) on its upper surface, and the lower surface as a whole acts as the collector terminal 44. Through wire bonding with aluminum wire 48, the solder 45 is connected to a pattern 47 on the control substrate 33 so that collector terminal 44 is connected to the pattern 47 through the solder 45 and the aluminum wire 48. Through wire bonding with other aluminum wire 48, the base electrode 46 is connected to a pattern 49 on the control substrate 33. The upper surfaces of the pattern 47 and 49 are plated with nickel and the nickel plating is further flash-plated with gold to make the bonding with the aluminum wire 49 sure and firm. Each power device chip 32 is covered with a sealing material 50 composed of epoxy resin, silica powder as a filler, a coloring agent such as carbon black as an additive, a fire retarding material such as an oxalic acid, etc., in order to protect the power device chip physically, chemically and electrically.

[0019] Thus, in the power module 31 in this arrangement, the opening 41 for insertion of the heat spreader 39 is formed in the control substrate 33 so that the heat spreader 39 is inserted into the opening 41 so as to be mounted on the base substrate 34. Accordingly, the power module 31 has a substantially single layer structure so that it is possible to reduce its height to half in comparison with a conventional two-layer system. Further, since the side edge of the heat spreader 39 is disposed at a distance from the side surface of the opening 41, the heat of the power device chip 32 on the heat

spreader 39 is not directly transmitted to the control substrate 33 through the heat spreader 39, and since the lower surface of the heat spreader 39 is fixed on the base substrate 34, the heat transmitted to the heat spreader 39 is further transmitted from the heat spreader 39 to the base substrate 34 and dissipated from the base substrate 34. Accordingly, the heat is less transmitted from the heat spreader 39 to the circuit elements on the control substrate 33, so that it is possible to dispose these circuit elements close to the power device chip 32, and it is therefore possible to reduce the area of the control substrate 33. Accordingly, it is possible to reduce the power module 31 in size in comparison with conventional ones. Further, since the power device chip 32 and the control substrate 33 are connected to each other through the aluminum wire 48, it is possible to absorb the difference in the thermal expansion coefficients between the heat spreader 39 and the control substrate 33 to thereby make it possible to improve the reliability against thermal stress.

[0020] The assembling work of this power module 31 will be described. First, the power device chip 32 is fixed at its collector terminal 44 side to the metal portion 43 of the heat spreader 39 through the solder 45. Next, the circuit elements 36 of the circuits for driving and controlling the chip are mounted on the control substrate 33, and the control substrate 33 is fixed to the base substrate 34 through the bonding agent 42. The wire bonding with the aluminum wire 48 is performed between the base electrode 46 of the power device chip 32 and the pattern 49 of the control substrate 33 and between the metal portion 43 and the pattern 47, and the wire bonding with the aluminum wire (not shown) is also performed between a not-shown emitter electrode and a pattern of the control substrate 33 (not shown) to which the emitter terminal 54 is to be connected, to thereby complete the electric connection. The heat spreader 39 is applied with the bonding agent 42, inserted to the opening 41, and fixed to the base substrate 34. The case cover 35 is then attached.

[0021] In the embodiment shown in Fig. 3, a pattern 47 for connection to a collector terminal 44 of each power device chip 32 is provided near an opening 41 of a control substrate 33, and this pattern 47 and a metal portion 43 are connected to each other through solder 51. A heat spreader 39 and the control substrate 33 are fixed to each other firmly through soldering, so that the heat spreader 39 and the control substrate 33 are held surely even if there occurs contraction due to the difference in thermal expansion coefficient between the heat spreader 39 and the control substrate 33. It is therefore possible to improve the reliability against thermal stress. Further, in this embodiment, a metal portion 52 similar to the metal portion 43 may be provided on the lower surface of the heat spreader 39, and fixed to the base substrate 34 through solder 53. Consequently, it is possible to improve the stability of the heat spreader 39 to the base substrate 34. The reference numeral 54 rep-

resents an emitter electrode of the power device chip 32, the emitter electrode 54 being connected through aluminum wire 48 to a pattern 55 of the control substrate 33 to which the emitter terminal 54 is to be connected.

[0022] Although the present invention has been described with reference to a preferred embodiment, the description is to make the invention understood, and the invention can be modified variously without departing from the appended claims.

Claims

1. A power module (31) in the form of a single package containing at least one power device chip (32) and circuits for driving and controlling said chip (32), said power module comprising:

a heat spreader (39) made from a heat-dissipating material and which supports said power device chip (32) on an upper surface thereof; a control substrate (33) having printed patterns (47,55) formed on an upper surface thereof which form said circuits and a printed connection pattern (47) for connection with said power device chip (32), and having an opening (41) in which said heat spreader (39) is located;

a base substrate (34) in the form of a heat-dissipating plate which fixedly supports said control substrate (33) and said heat spreader (39); at least said power device chip (32) being covered over its upper surface with a sealing compound (50);

said power device chip having electrodes formed on an upper surface thereof and a terminal formed by substantially the whole of a lower surface thereof;

said power device chip (32) and said printed connection pattern (47) being connected to each other through wire bonding (48);

said heat spreader (39) being provided at its upper surface with an upper metal portion (43), which is both thermally and electrically conductive, said upper metal portion (43) having a soldered connection (45) to a lower surface of said power device chip (32);

said printed connection pattern (47) being formed adjacent said opening (41) of said control substrate (33) and being electrically connected to said upper metal portion (43) of said heat spreader (39);

characterised in that:

the base substrate (34) is a metal plate;
the sealing compound (50) further covers part of said control substrate (33);
the electrodes on the upper surface of the pow-

er device chip (32) are base and emitter electrodes (46,54) and the terminal on the lower surface thereof is a collector terminal (44); the upper metal portion (43) has a soldered connection to the lower surface of said power device chip (32); the printed connection pattern (47) is connected to the upper metal portion through solder (51).

2. A power module as claimed in Claim 1, in which said heat spreader (39) is also provided on its lower surface with a lower metal portion (52) which is similar to said upper metal portion (43), said heat spreader (39) being connected through soldering (53) with said base substrate (34) through said lower metal portion (52).

Patentansprüche

1. Energie- bzw. Leistungsmodul (31) in der Form eines einzelnen Paketes bzw. Bausteins mit wenigstens einem Leistungsbauelementchip (32) und Schaltreisen bzw. Schaltungen zum Antreiben und Steuern bzw. Regeln des Chips (32), wobei das Leistungsmodul vorgesehen ist mit:

einem Wärmeverteiler (39) aus einem wärmeableitenden Material, welcher den Leistungsbauelementchip (32) auf einer oberen Fläche von diesem trägt,

einem Steuer- bzw. Regelträger bzw. -substrat (33) mit gedruckten Mustern bzw. gedruckten Schemata (47, 55), welche an einer oberen Fläche von diesem ausgebildet sind, welche die Schaltungen und ein gedrucktes Verbindungsmuster bzw. -schema (47) zur Verbindung mit dem Leistungsbauelementchip (32) bilden, und mit einer Öffnung (41), in der der Wärmeverteiler (39) angeordnet ist,

einem Grundsubstrat bzw. -träger (34) in Form einer wärmeableitenden Platte, welche das Steuer- bzw. Regelsubstrat (33) und den Wärmeverteiler (39) fest trägt,

wobei wenigstens der Leistungsbauelementchip (32) über seiner oberen Fläche mit einer Versiegelung bzw. Abdichtungsmischung bzw. Vergußmasse (50) abgedeckt ist, der Leistungsbauelementchip-Elektroden, welche an einer oberen Fläche von diesem ausgebildet sind, und einen Anschluß aufweist, welcher im wesentlichen durch das Ganze einer unteren Fläche von diesem gebildet wird,

der Leistungsbauelementchip (32) und das gedruckte Verbindungsschema (47) miteinander durch Drahtverbindung (48) verbunden sind, der Wärmeverteiler (39) an seiner oberen Fläche

mit einem oberen Metallabschnitt (43) versehen ist, welcher sowohl wärmeleitend als auch elektrisch leitend ist, wobei der obere Metallabschnitt (43) eine gelötete Verbindung (45) zu einer unteren Fläche des Leistungsbauelementchips (32) aufweist,

das gedruckte Verbindungsschema (47) angrenzend zu der Öffnung (41) des Steuer- bzw. Regelsubstrats (43) ausgebildet ist und elektrisch mit dem oberen Metallabschnitt (43) des Wärmeverteilers (39) verbunden ist,

dadurch gekennzeichnet, daß der Grundträger (34) eine Metallplatte ist,

die Versiegelungsvergußmasse (50) ferner einen Teil des Steuer- bzw. Regelsubstrates (33) abdeckt,

die Elektroden an der oberen Fläche des Leistungsbauelementchips (32) Basis und Emitter-Elektroden (46, 54) sind und der Anschluß an der unteren Fläche von diesem ein Kollektoranschluß (44) ist,

der obere Metallabschnitt (43) eine gelötete Verbindung zu der unteren Fläche des Leistungsbauelementchips (32) aufweist und das gedruckte Verbindungsschema (47) mit dem oberen Metallabschnitt durch Lot (51) verbunden ist.

2. Leistungsmodul gemäß Anspruch 1, bei welchem der Wärmeverteiler (39) ebenfalls an seiner unteren Fläche mit einem unteren Metallabschnitt (52) vorgesehen ist, welcher ähnlich zu dem oberen Metallabschnitt (43) ist, wobei der Wärmeverteiler (39) durch Löten (53) mit dem Grundträger (34) durch den unteren Metallabschnitt (52) verbunden ist.

Revendications

1. Module de puissance (31) sous la forme d'un boîtier simple contenant au moins une puce de dispositif de puissance (32) et des circuits servant à attaquer et commander ladite puce (32), ledit module de puissance comprenant :

un distributeur de chaleur (39) formé d'un matériau dissipant la chaleur et qui supporte ladite puce de dispositif de puissance (32) sur une surface supérieure de celle-ci ;

un substrat de commande (33) sur une surface supérieure duquel sont formés des motifs imprimés (47, 55) qui forment lesdits circuits et un motif de connexion imprimé (47) servant à la connexion avec ladite puce de dispositif de puissance (32), et pourvu d'une ouverture (41) dans laquelle est située ledit distributeur de

chaleur (39) ;
 un substrat de base (34) sous la forme d'une
 plaque dissipant la chaleur qui supporte de ma-
 nière fixe ledit substrat de commande (33) et
 ledit distributeur de chaleur (39) ; 5
 au moins ladite puce de dispositif de puissance
 (32) étant couverte d'un matériau d'étanchéité
 (50) sur sa surface supérieure ;
 ladite puce de dispositif de puissance étant 10
 pourvue d'électrodes formées sur une surface
 supérieure de celle-ci, et d'une borne formée
 essentiellement par la totalité d'une surface in-
 férieure de celle-ci ;
 ladite puce de dispositif de puissance (32) et 15
 ledit motif de connexion imprimé (47) étant re-
 liés l'un à l'autre par soudage à fils (48) ;
 ledit distributeur de chaleur (39) étant pourvu,
 à sa surface supérieure, d'une partie métallique
 supérieure (43) qui est conductrice sur les 20
 plans à la fois thermique et électrique, ladite
 partie métallique supérieure (43) ayant une
 connexion soudée (45) avec une surface infé-
 rieure de ladite puce de dispositif de puissance
 (32) ;
 ledit motif de connexion imprimé (47) étant for- 25
 mé de manière adjacente à ladite ouverture
 (41) dudit substrat de commande (33) et étant
 connecté électriquement à ladite partie métal-
 lique supérieure (43) dudit distributeur de cha-
 leur (39) ; 30

caractérisé en ce que

le substrat de base (34) est une plaque 35
 métallique ;
 le matériau d'étanchéité (50) couvre, en outre,
 une partie dudit substrat de commande (33) ;
 les électrodes sur la surface supérieure de la
 puce de dispositif de puissance (32) sont des 40
 électrodes de base et d'émetteur (46, 54) et la
 borne sur sa surface inférieure est une borne
 de collecteur (44) ;
 la partie métallique supérieure (43) a une con-
 exion soudée avec la surface inférieure de la- 45
 dite puce de dispositif de puissance (32) ;
 le motif de connexion imprimé (47) est connec-
 té à la partie métallique supérieure par soudure
 (51).

2. Module de puissance selon la revendication 1, dans 50
 lequel ledit distributeur de chaleur (39) est égale-
 ment pourvu, sur sa surface inférieure, d'une partie
 métallique inférieure (52) qui est similaire à ladite
 partie métallique supérieure (43), ledit distributeur 55
 de chaleur (39) étant connecté, par soudure (53),
 audit substrat de base (34) par le biais de ladite par-
 tie métallique inférieure (52).

FIG. 1

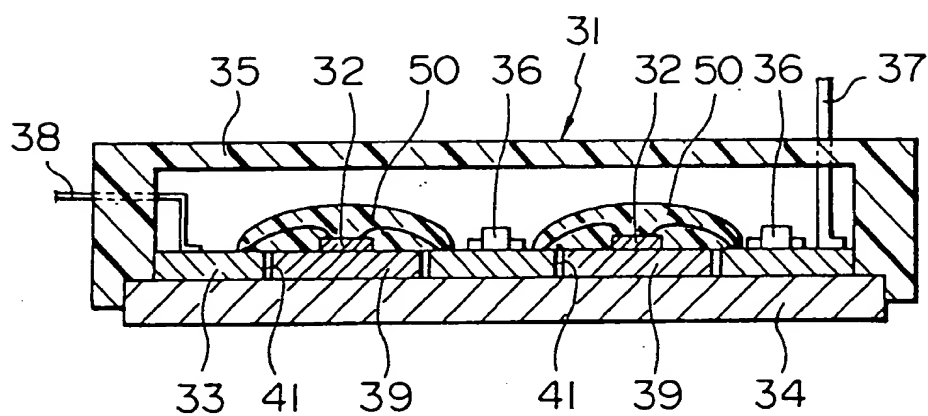


FIG. 2

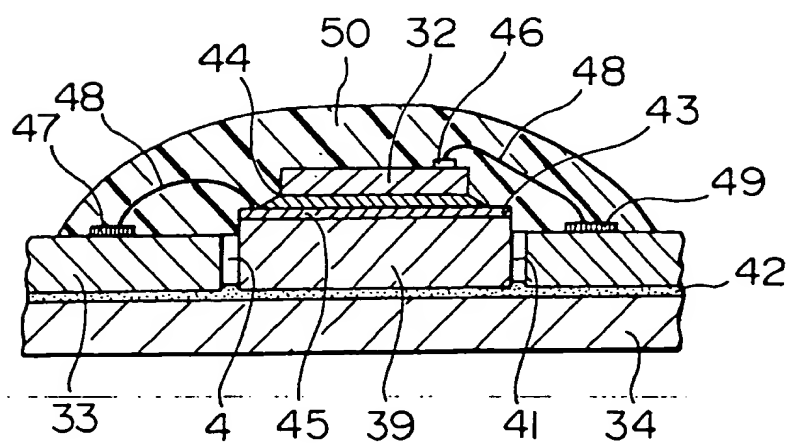


FIG. 3

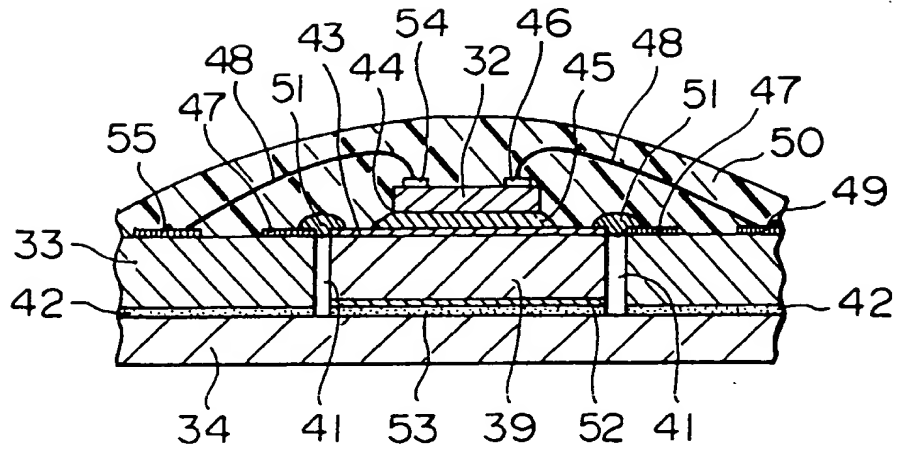


FIG. 4 PRIOR ART

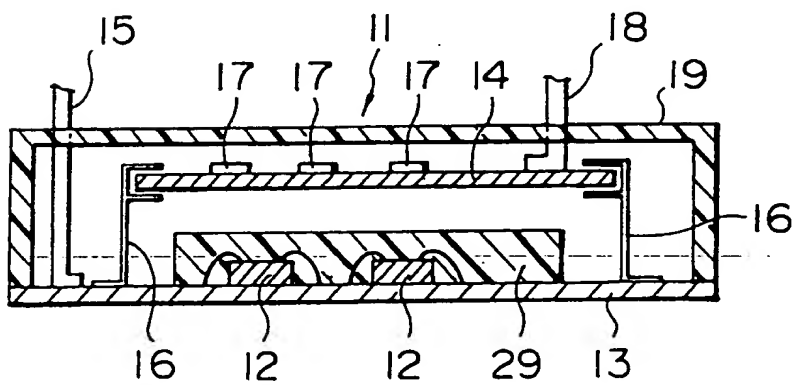


FIG. 5 PRIOR ART

